

RVX7050M

The RVX7050M is a high frequency and low phase noise performance VCXO. Its RMS phase jitter achieves 0.1 ps typ. (12 kHz to 20 MHz offset) with a high-end operating temperature of 105°C. This 7.0 x 5.0 mm SMD footprint tight frequency stability VCXO is available in hundreds of industry-standard frequencies from 8 to 800 MHz, and has a short lead time.

Features

- Fast sample turnaround
- LVC MOS, LVPECL, or LVDS output options
- 0.1 ps typ. RMS phase jitter (12 kHz to 20 MHz)
- Wide frequency range

Applications

- Base stations
- Ethernet
- DSL/ADSL
- WiMAX/W-LAN
- Wi-Fi

7.0 x 5.0 x 1.6 mm



Standard Specifications

| Parameter | Min. | Typ. | Max. | Unit | Test Condition / Description |
|--------------------------------|------|------|------|------|---|
| Nominal frequency | 8 | | 200 | MHz | LVC MOS |
| | 8 | | 800 | MHz | LVPECL or LVDS |
| Temperature range | -40 | | 105 | °C | |
| Temperature stability | | | ±25 | ppm | Temperature range: -40 to 85°C |
| | | | ±30 | ppm | Temperature range: -40 to 105°C |
| Frequency stability | | | ±50 | ppm | Including frequency calibration, operating temperature range, supply and load variations, and 10 years ageing at 25°C |
| Absolute pull range (APR) | ±50 | | | ppm | Referenced at Vc = 1.65V |
| Supply voltage (VDD) | | 3.3 | | V | With a tolerance of ±5% |
| Supply current | | | 30 | mA | For LVC MOS |
| | | | 85 | mA | For LVPECL |
| | | | 50 | mA | For LVDS |
| RMS phase jitter (@122.88 MHz) | | 0.1 | 0.15 | ps | Integrated from 12kHz to 20MHz |

Model Outline and Recommended Pad Layout

TOP VIEW

SIDE VIEW

RECOMMENDED PAD LAYOUT
- TOP VIEW

BOTTOM VIEW

PIN CONNECTIONS

| | |
|----|--|
| 1* | E/D or NC |
| 2* | E/D or NC |
| 3 | GND |
| 4 | Output |
| 5* | NC (LVC MOS) or Complementary Output (LVPECL/LVDS) |
| 6 | VDD |

* Depending on specifications

NOTE:
Outline unit is mm.