

## RVX7050P

The RVX7050P is a high frequency and low phase noise performance VCXO. Its RMS phase jitter achieves 0.5 ps typ. (12 kHz to 20 MHz offset). This 7.0 x 5.0 mm SMD footprint tight frequency stability VCXO is available in hundreds of industry-standard frequencies from 8 to 1500 MHz, and has a short lead time.

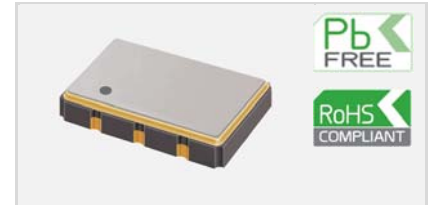
### Features

- Fast sample turnaround
- LVC MOS, LVPECL, or LVDS output options
- 0.5 ps typ. RMS phase jitter (12 kHz to 20 MHz)
- Wide frequency range

### Applications

- High Speed ADC/DAC/SERDES
- Broadcast Video
- Radio Systems
- DSL/ADSL
- PON/FTTH

7.0 x 5.0 x 1.6 mm



### Standard Specifications

Parameter	Min.	Typ.	Max.	Unit	Test Condition / Description
Nominal frequency	8		200	MHz	LVC MOS
	8		1500	MHz	LVPECL or LVDS
Temperature range	-40		85	°C	
Temperature stability			±35	ppm	Temperature range: -40 to 85°C
Frequency stability			±50	ppm	Including frequency calibration, operating temperature range, supply and load variations, and 10 years ageing at 25°C
Absolute pull range (APR)	±50			ppm	Referenced at Vc = 1.65V
Supply voltage (VDD)		2.5		V	With a tolerance of ±5%
		3.3			
Supply current			30	mA	For LVC MOS
			65	mA	For LVPECL
			40	mA	For LVDS
RMS phase jitter		0.5	1.0	ps	Integrated from 12kHz to 20MHz

### Model Outline and Recommended Pad Layout

**TOP VIEW**

**SIDE VIEW**

**RECOMMENDED PAD LAYOUT**  
- TOP VIEW

**BOTTOM VIEW**

**PIN CONNECTIONS**

1	Vc
2*	E/D or NC
3	GND
4	Output
5*	NC (LVC MOS) or Complementary Output (LVPECL/LVDS)
6	VDD

\* Depending on specifications

**NOTE:**  
Outline unit is mm.